

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Shane Ching-Feng Hu Attorney Docket No.: 501147.02 (30119/US/2)
Filed : Concurrently Herewith
Title : STATIC CONTENT ADDRESSABLE MEMORY CELL

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

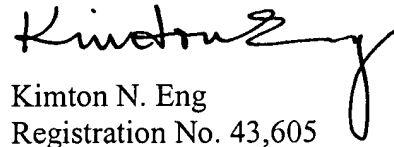
Sir:

In accordance with 37 C.F.R. §§ 1.56 and 1.97 through 1.98, applicant wishes to make known to the Patent and Trademark Office the references set forth on the attached form PTO-1449. This application relies, under 35 U.S.C. § 120, on the earlier filing date of prior Application No. 10/094,574, filed March 8, 2002. The references listed on the attached Form PTO-1449 were submitted to and/or cited by the Patent and Trademark Office in this prior application and, therefore, are not required to be provided in this application. If the Examiner wishes, copies will be provided upon request. Although the aforesaid references are made known to the Patent and Trademark Office in compliance with applicant's duty to disclose all information he is aware of which is believed relevant to the examination of the above-identified application, applicant believes that his invention is patentable.

Please acknowledge receipt of this Information Disclosure Statement and kindly make the cited references of record in the above-identified application.

Respectfully submitted,

DORSEY & WHITNEY LLP



Kimton N. Eng
Registration No. 43,605

KNE:ajs

Enclosures:

Postcard
Form PTO-1449

1420 Fifth Avenue, Suite 3400
Seattle, WA 98101
Telephone (206) 903-8800
Facsimile (206) 903-8820

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FORM PTO-1449
(REV.7-80)U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.
501147.02 (30119/US/2)APPLICATION NO.
Not Yet Assigned**INFORMATION DISCLOSURE STATEMENT***(Use several sheets if necessary)*APPLICANT(S)
Shane Ching-Feng HuFILING DATE
Concurrently herewithGROUP ART UNIT
Not Yet Assigned**U.S. PATENT DOCUMENTS**

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	AI							

OTHER PRIOR ART *(Including Author, Title, Date, Pertinent Pages, Etc.)*

	AJ	Ikenaga, T. et al., <i>A Fully Parallel 1-Mb CAM LSI for Real-Time Pixel-Parallel Image Processing</i> , IEEE Journal of Solid-State Circuits, Vol. 35, No. 4, April 2000, pp. 536-544.
	AK	Lines, V. et al., <i>66MHz 2.3M Ternary Dynamic Content Addressable Memory</i> , IEEE, May 2000, pp. 101-105.
	AL	Mundy, J. L. et al., <i>Low-Cost Associative Memory</i> , IEEE Journal of Solid-State Circuits, Vol. SC-7, No. 5, October 1972, pp. 364-369.
	AM	Shafai, F. et al., <i>Fully Parallel 30-MHz, 2.5-Mb CAM</i> , IEEE Journal of Solid-State Circuits, Vol. 33, No. 11, November 1998, pp. 1690-1696.
	AN	Tamura, L. R. et al., <i>A 4-ns BiCMOS Translation-Lookaside Buffer</i> , IEEE Journal of Solid-State Circuits, Vol. 25, No. 5, October 1990, pp. 1093-1101.

EXAMINER

DATE CONSIDERED

* EXAMINER: Initial if reference considered, whether or not criteria is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).